

IN THE SPECIFICATION

Please enter the following amendments to the Specification:

Please replace the paragraph on page 14, lines 1-5 with the following replacement paragraph:

Additional details concerning the features and operation of system 112 may be found in e.g., commonly-owned U.S. Patent No. 6,636,933, ~~co-pending U.S. Patent Application Serial No. 09/745,814~~ entitled, "Data Storage System Having Crossbar Switch With Multi-Staged Routing," filed December 21, 2000; this co-pending Application is hereby incorporated by reference herein in its entirety.

Please replace the paragraph on page 18, lines 10-21 with the following replacement paragraph:

Although not shown in Figures, it should be noted that, in actual implementation of board 100A, portions of the circuitry 200 may be distributed in the regions 202, 204, 206, and 208 (e.g., circuitry for providing relatively low level commands/signals to actual SDRAM IC devices in the region, such as, chip select, clock synchronization, memory addressing, data transfer, memory control/management, clock enable signals, etc.), however, for purposes of the present discussion, this circuitry may be thought of as being logically comprised in the circuitry 200. Further details and description of the types and functions of circuitry 200 that may be distributed in the regions 202, 204, 206, and 208 in actual implementation of board 100A may be found in e.g., commonly-owned U.S. Patent No. 6,554,511, ~~co-pending U.S. Patent Application Serial No. 09/796,259~~, filed February 28, 2001, entitled "Error Condition Handling" (Attorney Docket No. EMC-01-034); said co-pending Application is hereby incorporated herein by reference in its entirety.

Please replace the paragraph on page 27, lines 3-11 with the following replacement paragraph:

For example, although illustrative embodiments of the present invention have been described in connection with use in a network data storage system that comprises a messaging network 14 that facilitates communications between the host controllers and the disk controllers, and a point-to-point data transfer network system that comprises links 40, 42, 44, and 46, if appropriately modified, these embodiments of the present invention may instead be used in connection with other types of network data storage systems, e.g., that utilize a redundant busy system of the type described in commonly-owned U.S. Patent No. 6,554,551, ~~co-pending U.S. Patent Application Serial No. 09/796,259~~, filed February 28, 2001, entitled "Error Condition Handling" (Attorney Docket No. EMC-01-034).

IN THE TITLE

Please replace the original Title with the following new Title:

SYSTEMS AND METHODS WHICH UTILIZE PARITY SETS